

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5/D.S.S
6-25-02
C. Wills

J1011 U.S. PTO
10/055722
01/23/02

In re application of

Akio KITAMURA

Batch:

Serial No.: NEW APPLICATION

Group Art Unit:

Filed: January 23, 2002

Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURE METHOD THEREFORE

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

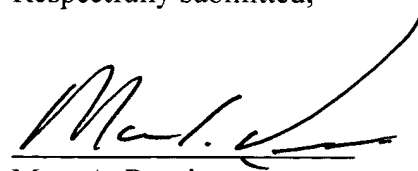
Sir:

Pursuant to 37 C.F.R. §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. Copies of the references listed on Form PTO-1449 are attached.

It is respectfully requested that the information be expressly considered during the prosecution of this application, that these references be made of record therein and appear among the "References Cited" on any patent to issue therefrom, and that an initialed copy of the PTO-1449 be returned to the undersigned.

Respectfully submitted,

Date: 01/23/02


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